

Figure 1. Transfer curve with negative offset

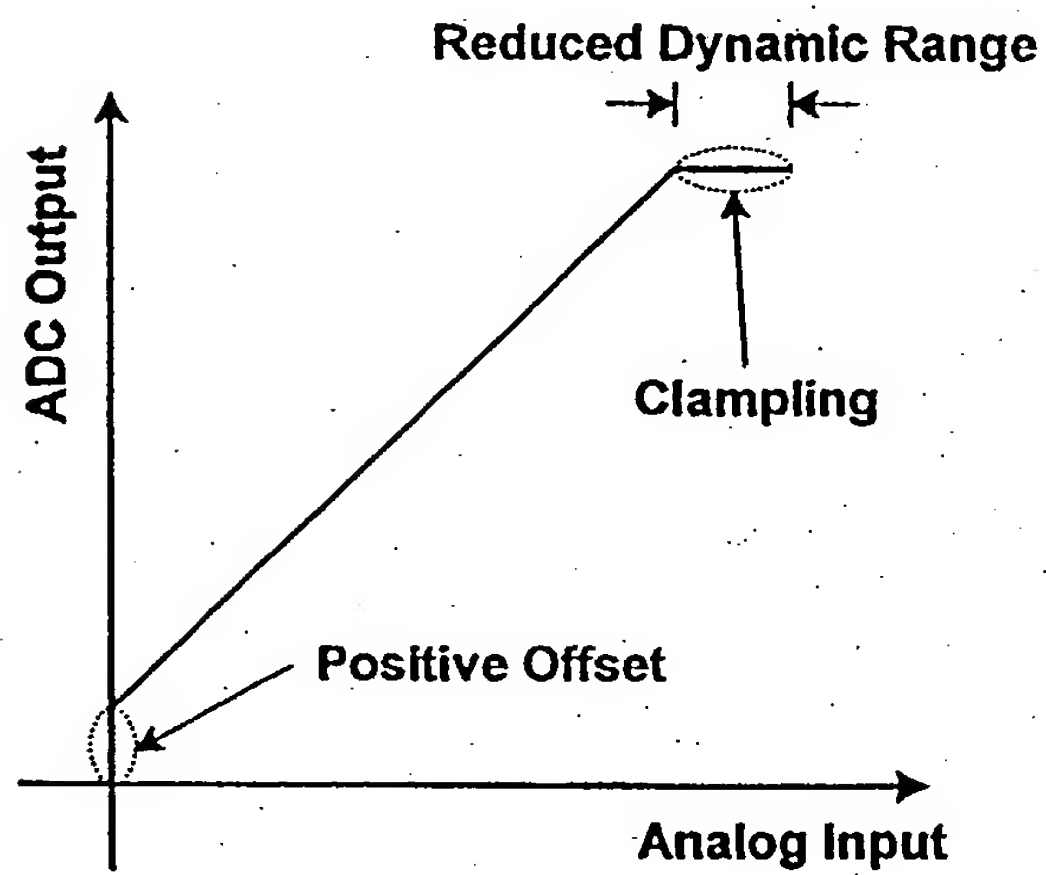
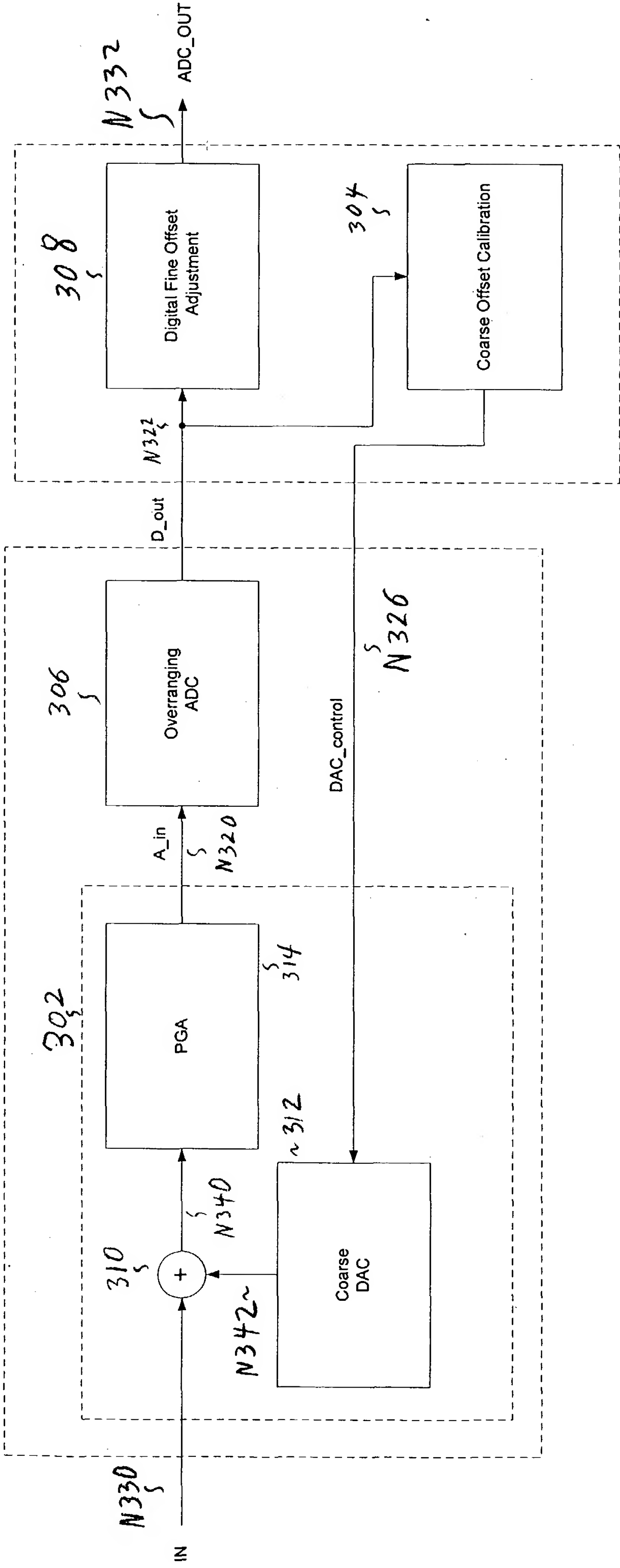


Figure 2. Transfer curve with positive offset

300

350

360



DRAFT

Figure 3

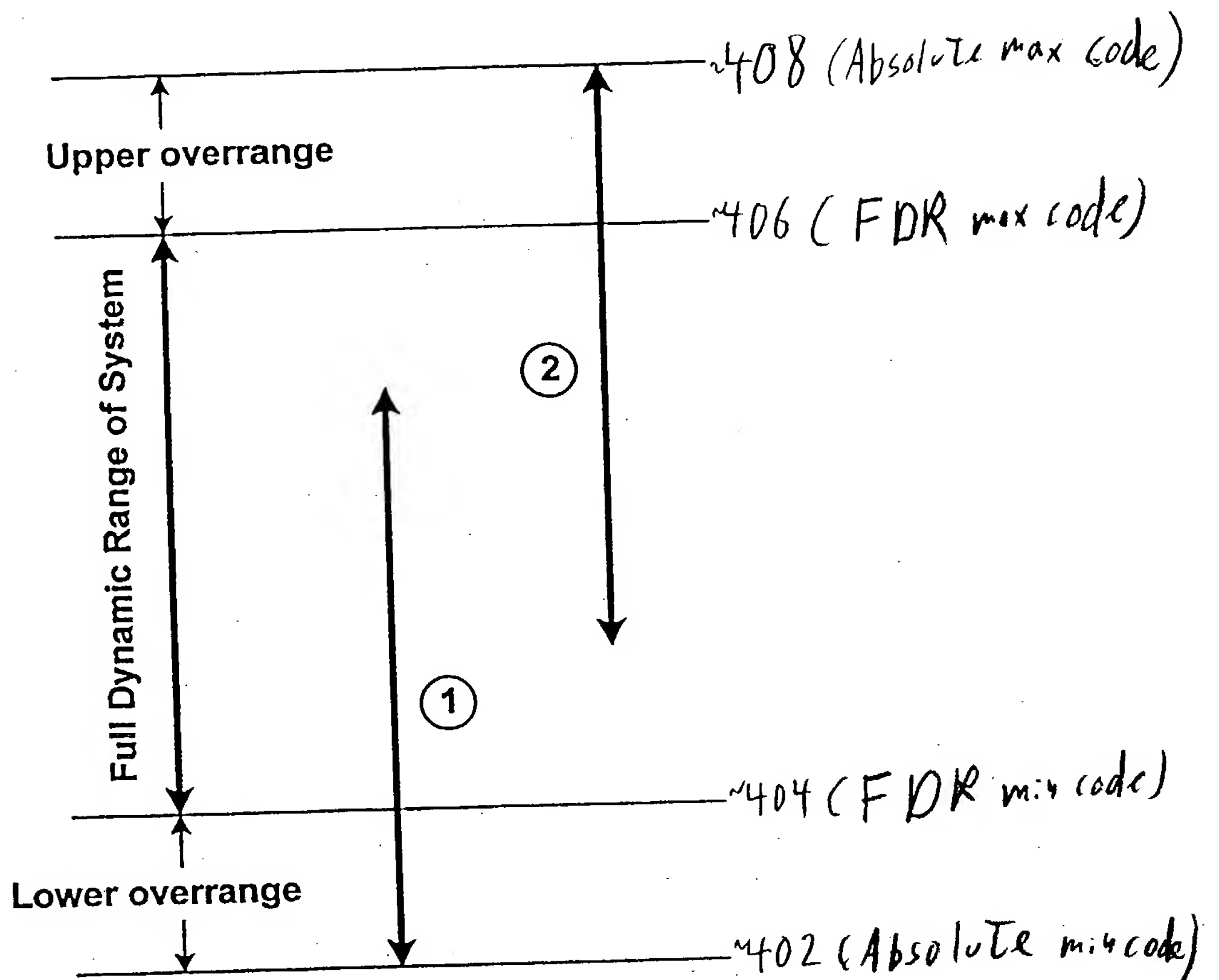


Figure 4.

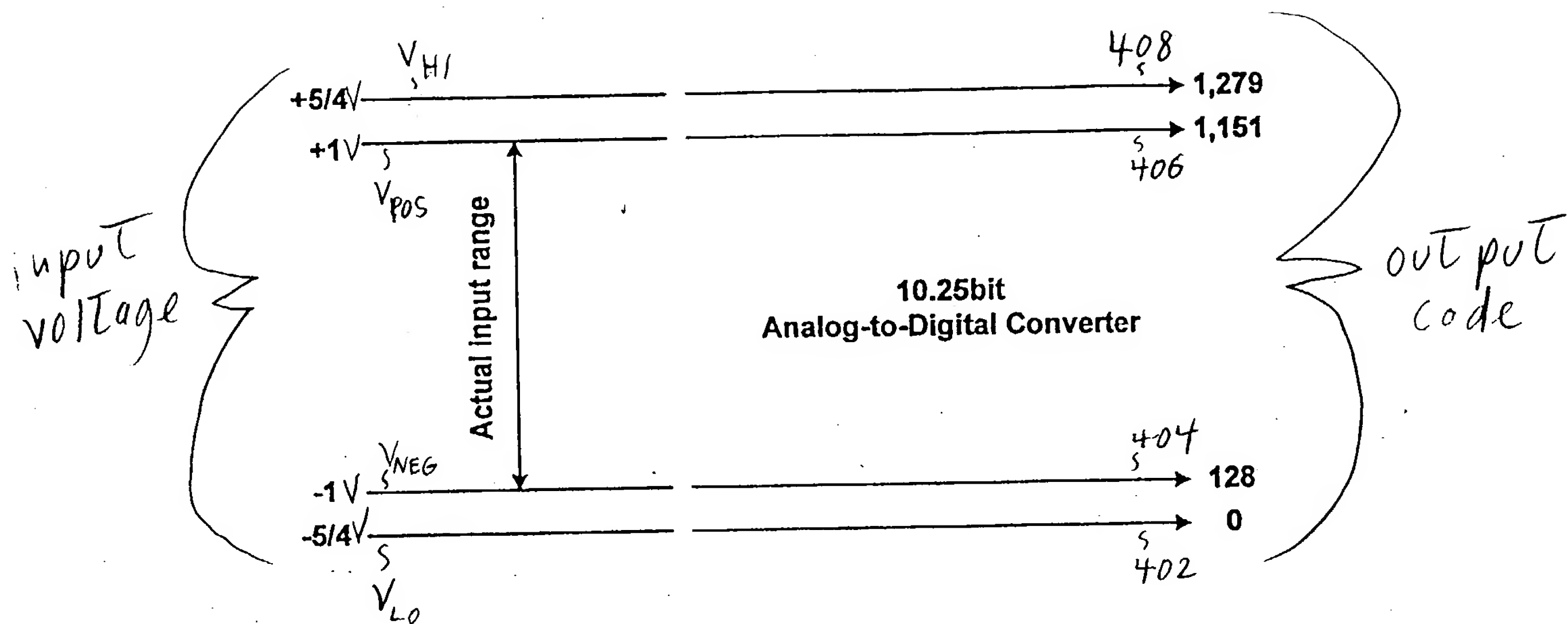
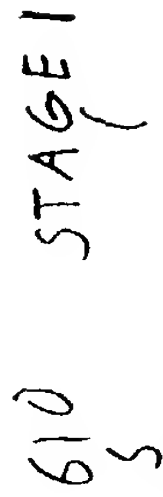
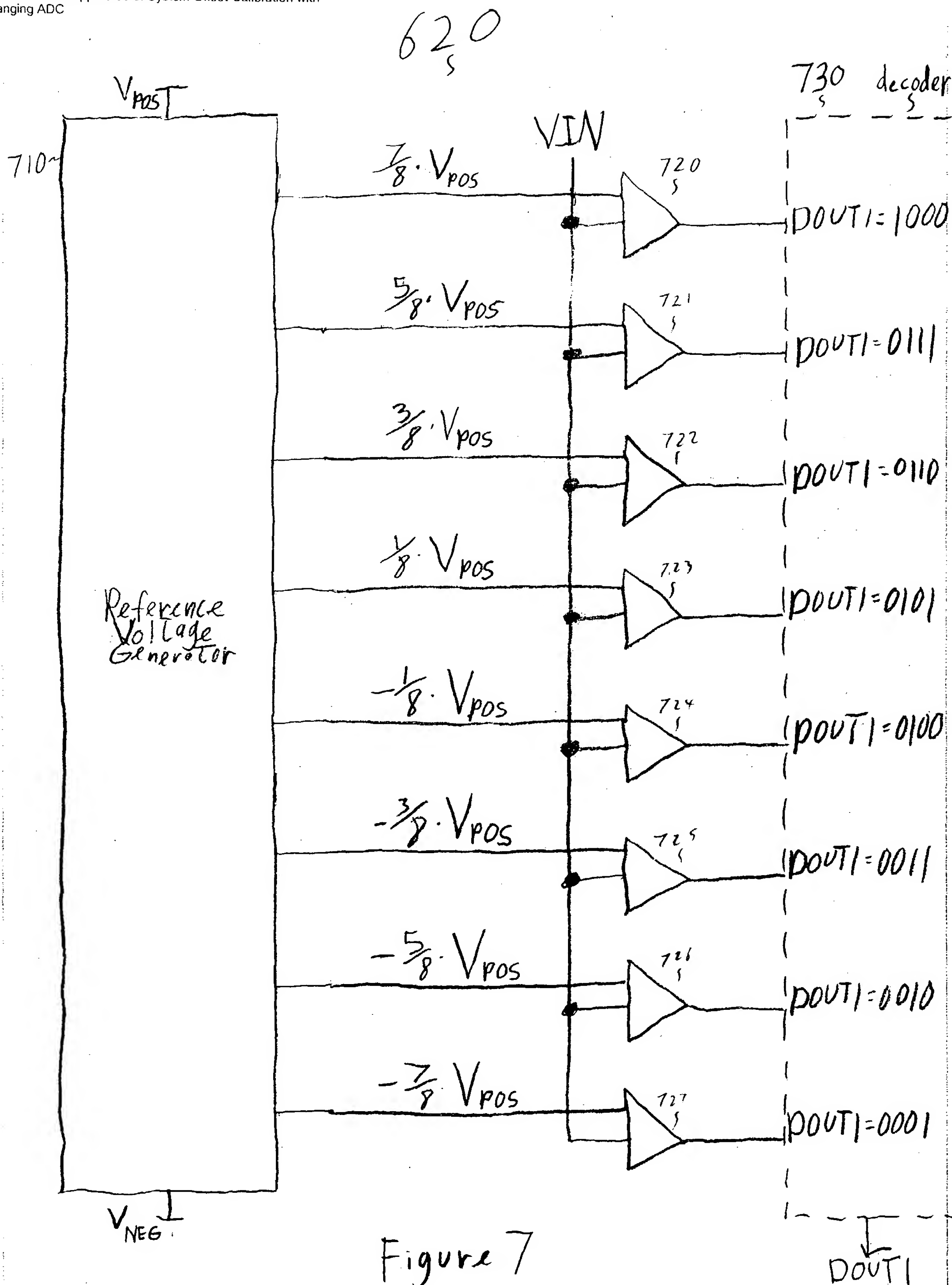


Figure 5



DRAFT

Figure 6



DRAFT

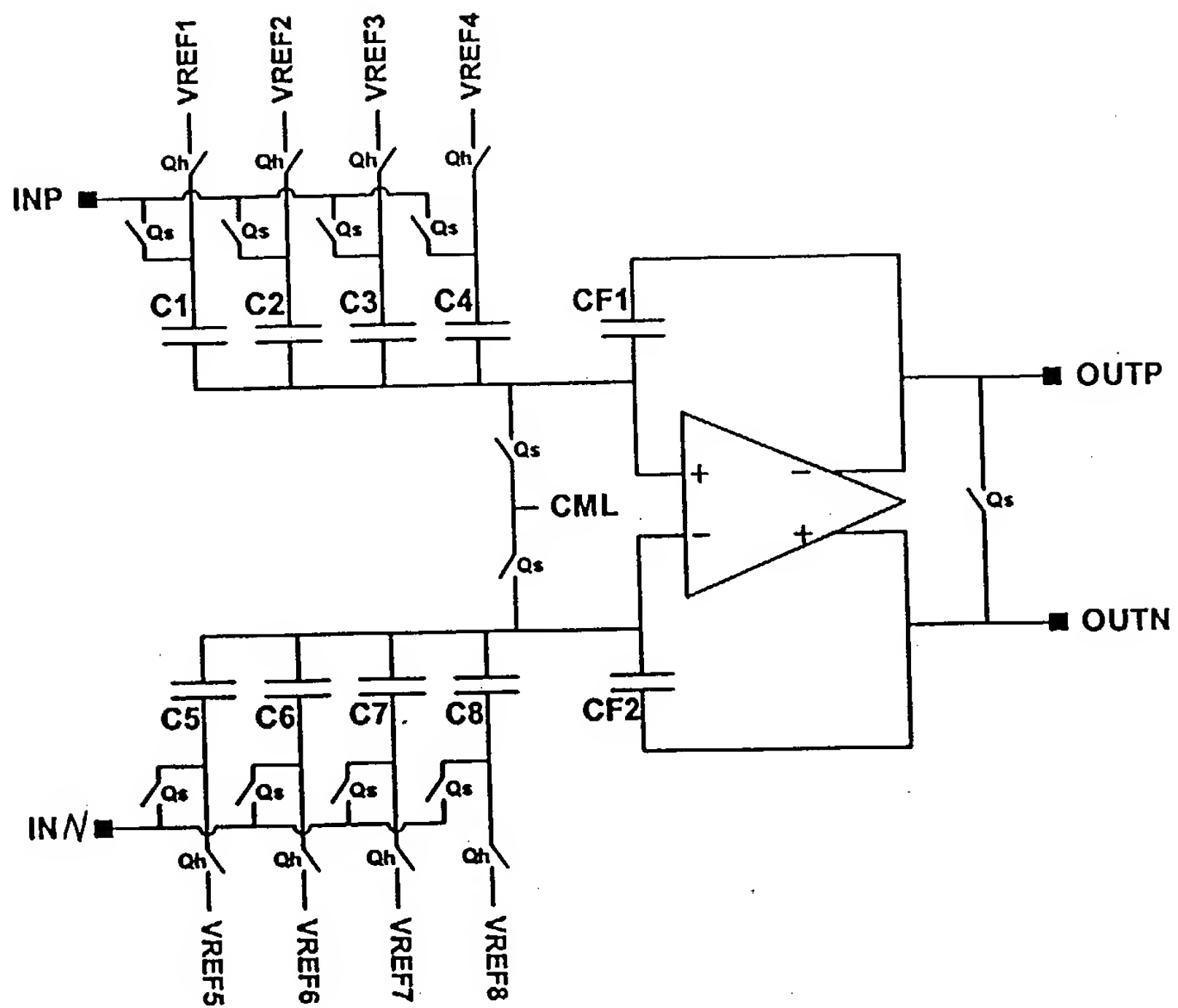


Figure 8A

DRAFT

622
s

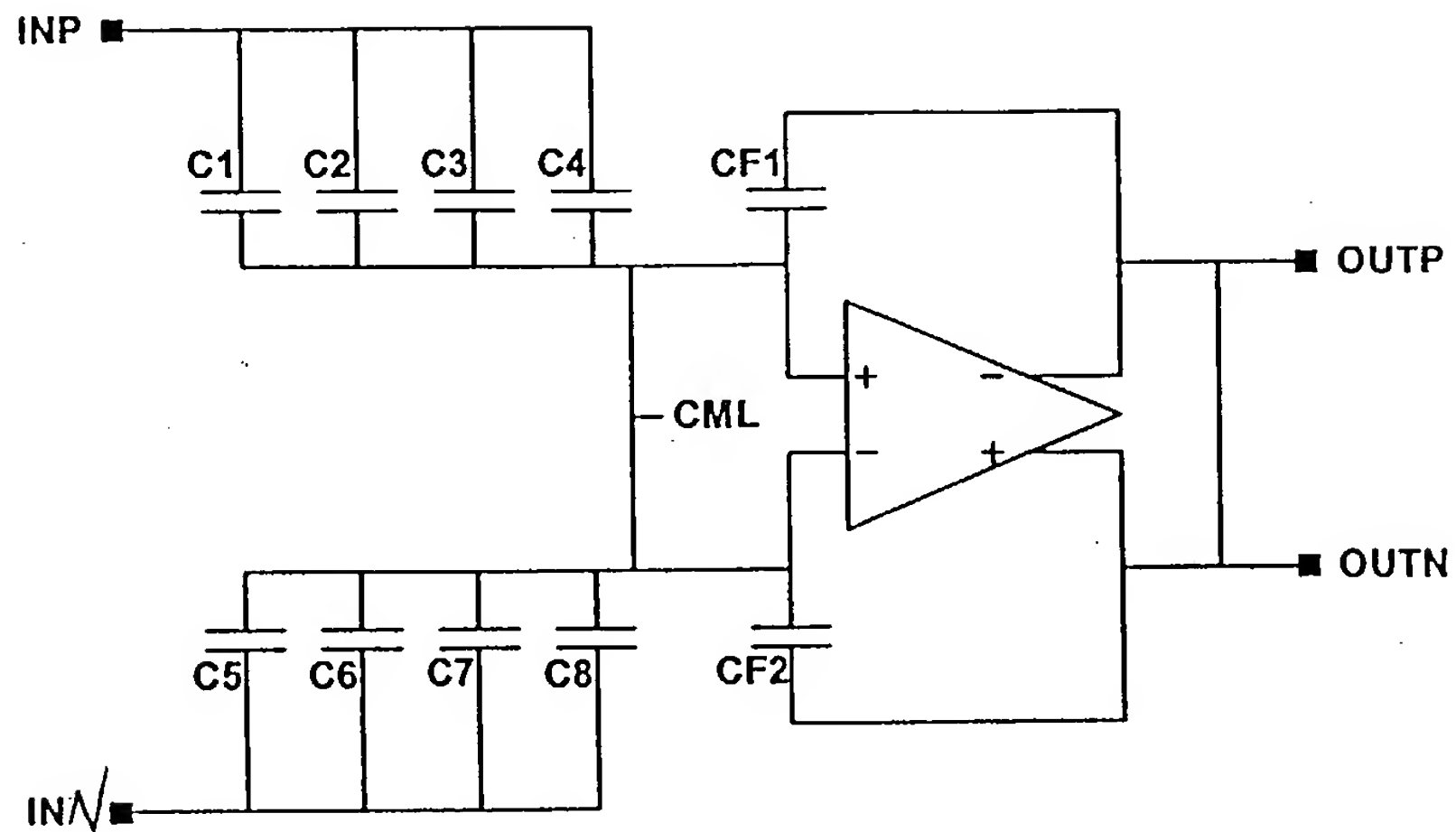


Figure 8B

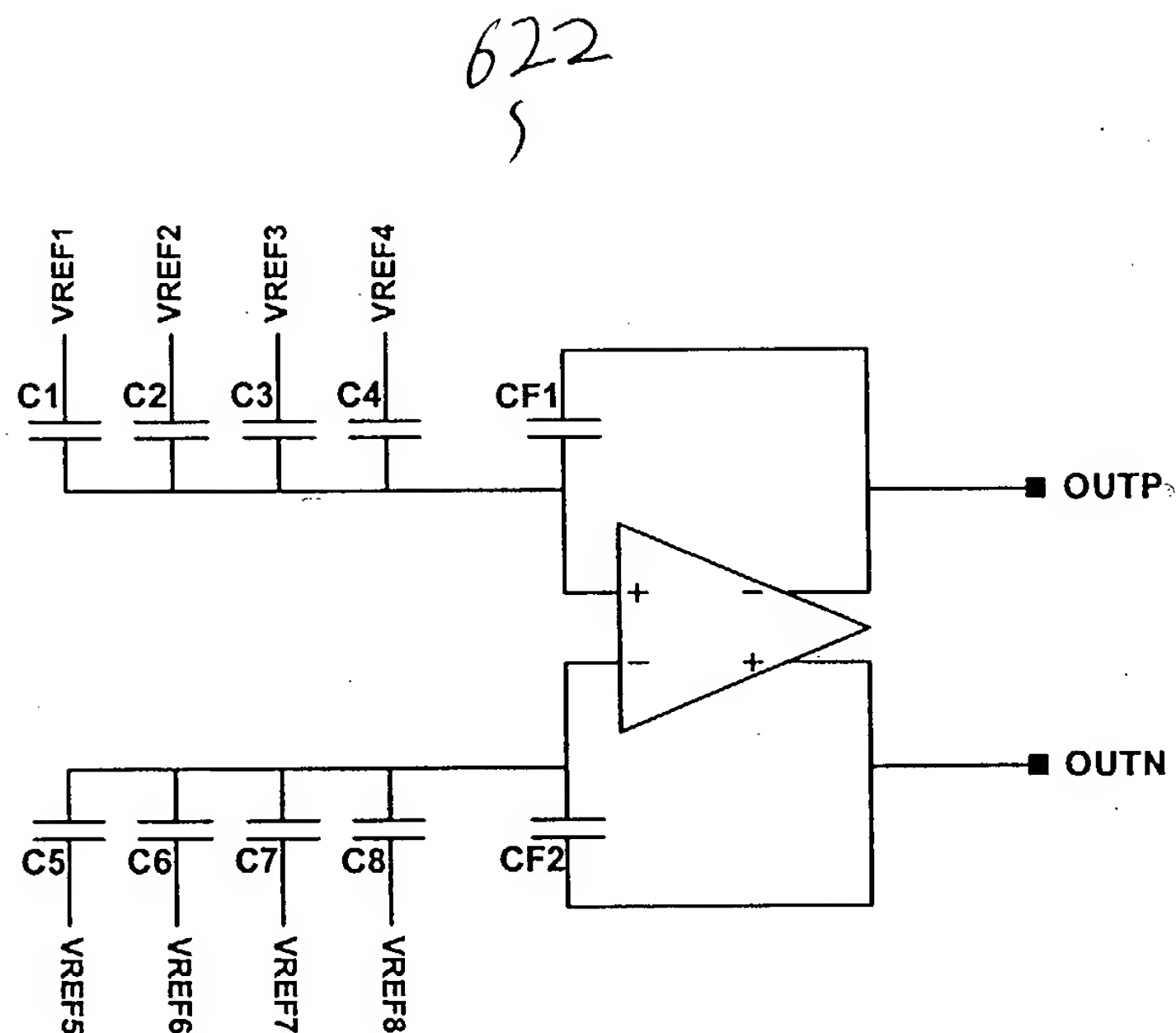
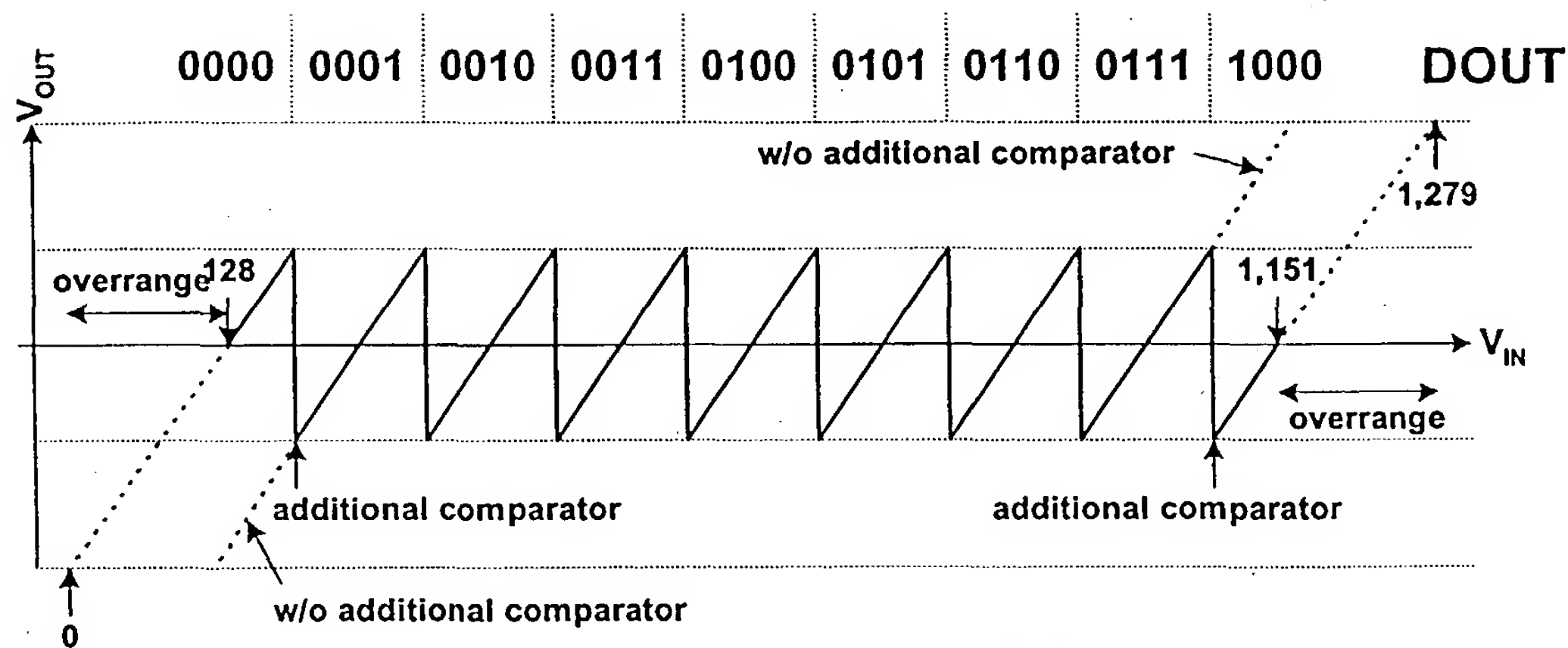


Figure 8C

	VREF1	VREF2	VREF3	VREF4	VREF5	VREF6	VREF7	VREF8	V _{OUT}	DOUT1
7/8<VIN	0.5	0.5	0.5	0.5	-0.5	-0.5	-0.5	-0.5	4V _{IN} -4	1000
5/8<VIN<7/8	0.5	0.5	0.5	0	0	-0.5	-0.5	-0.5	4V _{IN} -3	0111
3/8<VIN<5/8	0.5	0.5	0	0	0	0	-0.5	-0.5	4V _{IN} -2	0110
1/8<VIN<3/8	0.5	0	0	0	0	0	0	-0.5	4V _{IN} -1	0101
-1/8<VIN<1/8	0	0	0	0	0	0	0	0	4V _{IN}	0100
-1/8<VIN<-3/8	-0.5	0	0	0	0	0	0	0.5	4V _{IN} +1	0011
-3/8<VIN<-5/8	-0.5	-0.5	0	0	0	0	0.5	0.5	4V _{IN} +2	0010
-7/8<VIN<-5/8	-0.5	-0.5	-0.5	0	0	0.5	0.5	0.5	4V _{IN} +3	0001
VIN<-7/8	-0.5	-0.5	-0.5	-0.5	0.5	0.5	0.5	0.5	4V _{IN} +4	0000

Figure 9



Residue of 3.5bit First MDAC

Figure 10

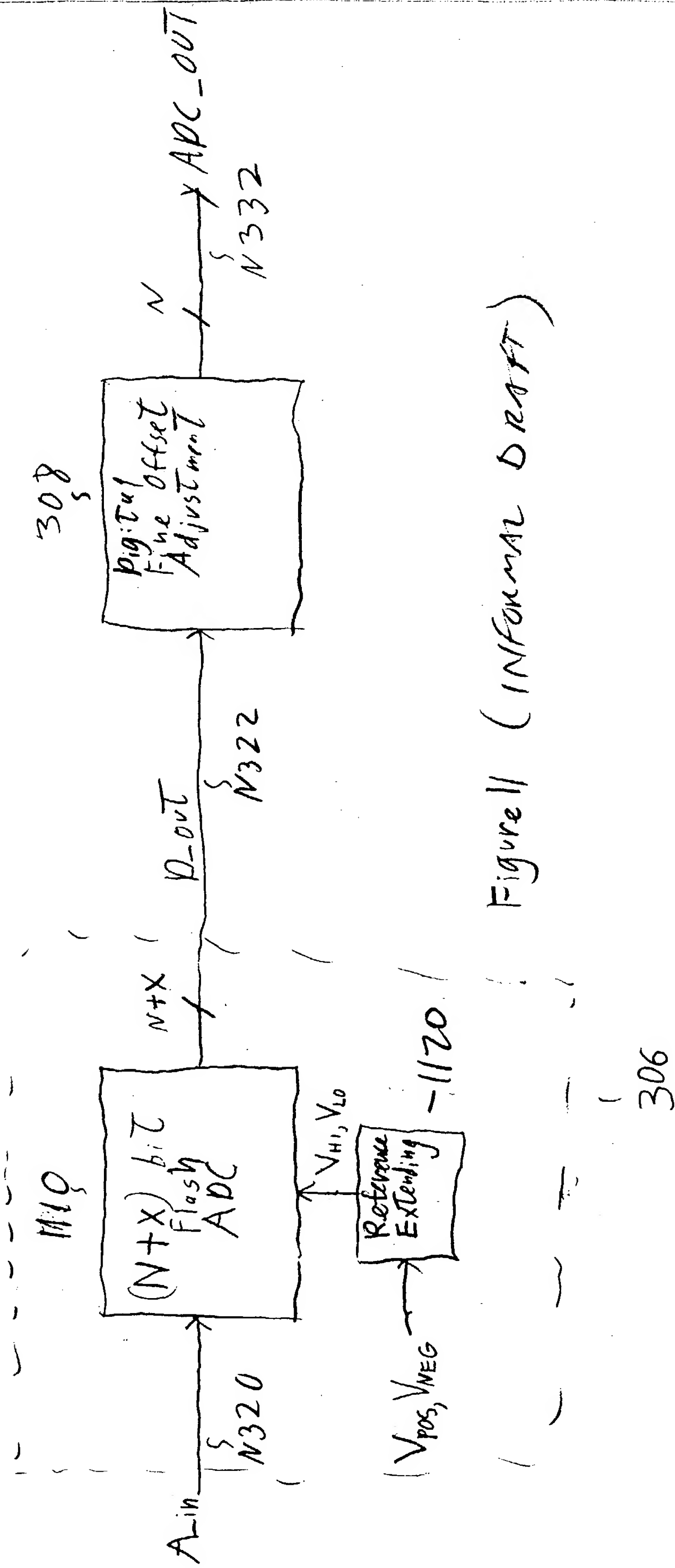


Figure 11 (Informal Draft)

DRAFT